



# DATASHEET



## VPT100+2800D SERIES HIGH RELIABILITY COTS DC-DC CONVERTERS

### Models Available

Input: 16 V to 40 V continuous, 50 V transient  
100 W, dual outputs of 12 V, 15 V  
-55 °C to 105 °C Operation

### 1.0 DESCRIPTION

The VPT100+ series of isolated COTS DC-DC converters is a cost-effective solution for many demanding high reliability applications. A wide input voltage range accommodates nominal 28V inputs including avionics, mobile, ground systems, and other applications. A high efficiency design reduces input power requirements and eases thermal management. Low input and output ripple, fixed operating frequency, and companion EMI filters simplify system design and compliance. A proven design heritage, no optoisolators and a rugged all metal package ensures long term reliability.

The VPT100+ series is intended for harsh environments including severe vibration, shock and temperature cycling. Testing is to JESD22, MIL-STD-810, and MIL-STD-883.

These converters are designed and manufactured in a facility certified to ISO9001, J-STD-001 and IPC-A-610.

This product may incorporate one or more of the following U.S. patents:

- 5,784,266
- 5,790,389
- 5,963,438
- 5,999,433
- 6,005,780
- 6,084,792
- 6,118,673



### 1.1 FEATURES

- High Reliability at Low Cost
- 100 Watts Output Power
- High Efficiency, Up to 89%
- Wide Input Voltage Range: 16 to 40 Volts per MIL-STD-704 and MIL-STD-1275
- High Input Transient Voltage: 50 Volts for 1 second
- Input Undervoltage Lockout
- Fixed Frequency
- Output Voltage Trim (+10% / -20%)
- Output Soft Start
- Current Limit Protection
- Short Circuit Protection
- Wide Temperature Range, -55 °C to 105 °C
- Internally Conformal Coated
- Standard Six-Sided Non-Hermetic Rugged Metal Package

### 1.2 COMPLIANCE

- MIL-STD-1275
- MIL-STD-704
- Meets MIL-STD-461 C-G when used with an appropriate VPT EMI filter

### 1.3 PACKAGING

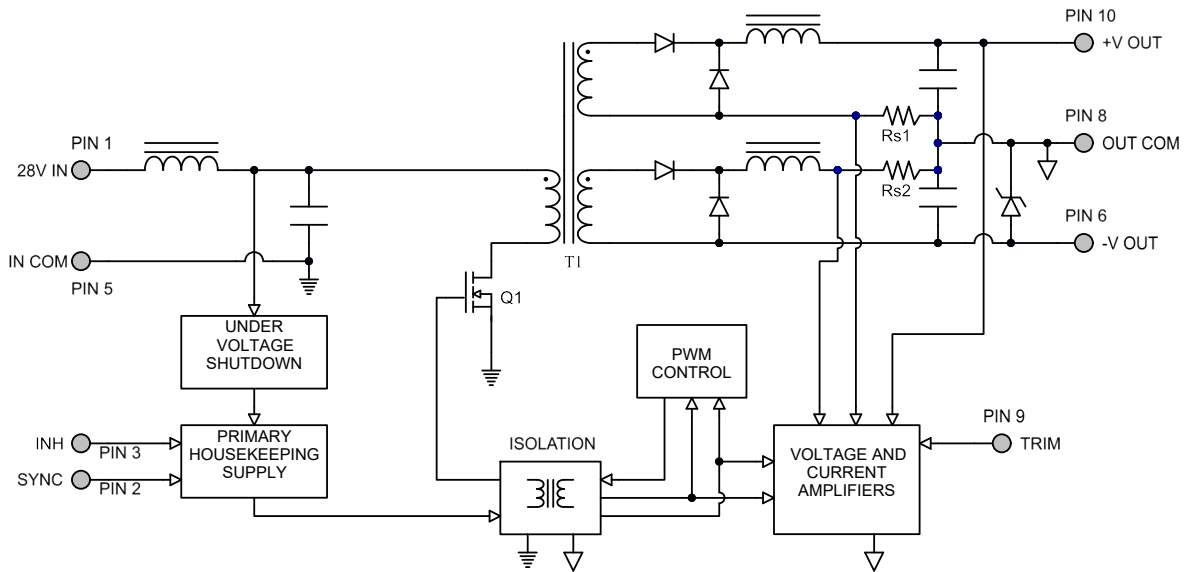
- Low-profile: 2.350" x 1.550" x 0.465"
- Max weight: 76 g

### 1.4 SIMILAR PRODUCTS AND ACCESSORIES

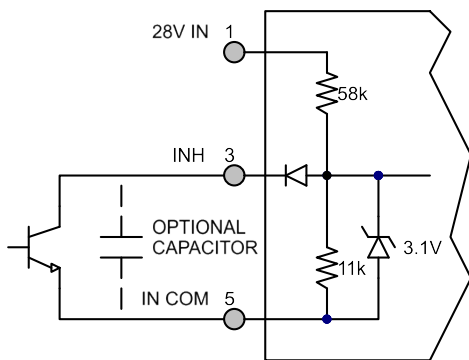
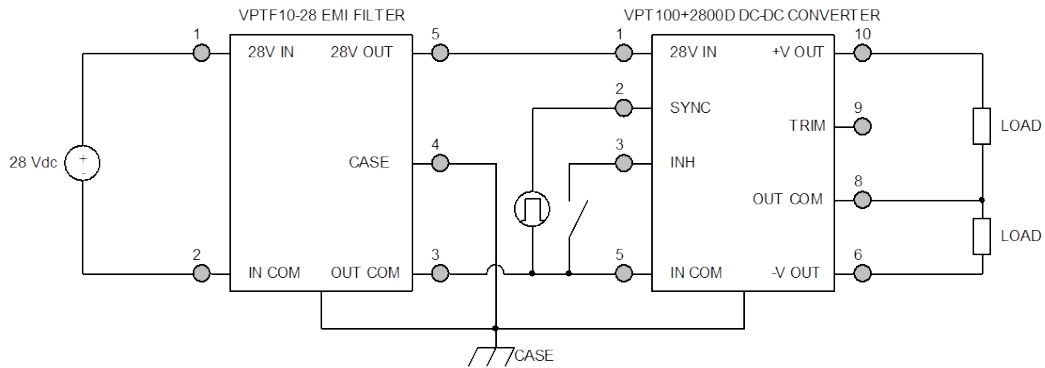
- DVFL 120 W dual output DC-DC Converter
- VXR100 100 W dual output COTS DC-DC Converter
- VHR100 100 W dual output COTS DC-DC Converter
- EMI filters, Thermal Pads, Front-End Modules and Accessories

2.0 DESCRIPTION

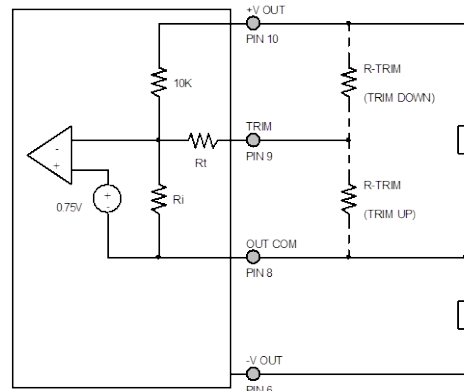
2.1 BLOCK DIAGRAM



2.2 CONNECTION DIAGRAM



Inhibit Circuit (Shown with optional capacitor for turn-on delay)



Output Voltage Trim Circuit

### 3.0 SPECIFICATIONS

#### 3.1 ABSOLUTE MAXIMUM RATINGS

Absolute Maximum Ratings			
Input Voltage (Continuous):	40 V	Operating Temperature (Full Load):	-55 °C to 105 °C
Input Voltage (Transient, 1 second):	50 V	Storage Temperature:	-55 °C to 125 °C
		Lead Solder Temperature (10 seconds):	300 °C

#### 3.2 PERFORMANCE SPECIFICATIONS

T<sub>case</sub> = -55 °C to 105 °C, V<sub>in</sub> = +28 V ± 5%, Full Load<sup>4</sup>, Unless Otherwise Specified

Parameter	Conditions	VPT100+2812D			VPT100+2815D			Units
		Min	Typ	Max	Min	Typ	Max	
<b>INPUT</b>								
Voltage	Continuous	16	28	40	16	28	40	V
	Transient, 1 sec <sup>3</sup>	-	-	50	-	-	50	V
Current	INH < 1.5 V	-	-	5	-	-	5	mA
	No Load	-	50	100	-	50	100	mA
Ripple Current	20 Hz to 10 MHz	-	-	200	-	-	200	mApp
Undervoltage Lockout	Turn On	14.5	-	15.8	14.5	-	15.8	V
	Turn Off <sup>3</sup>	14	-	15	14	-	15	V
<b>OUTPUT STATIC</b>								
Voltage	+V <sub>out</sub> , T <sub>case</sub> = 25 °C	11.82	12	12.18	14.775	15	15.225	V
	+V <sub>out</sub> , T <sub>case</sub> = -55 °C to 105 °C	11.7	12	12.3	14.625	15	15.375	V
	-V <sub>out</sub> , T <sub>case</sub> = 25 °C	11.7	12	12.3	14.625	15	15.375	V
	-V <sub>out</sub> , T <sub>case</sub> = -55 °C to 105 °C	11.58	12	12.42	14.475	15	15.525	V
Power <sup>2,5</sup>	Total	-	-	100	-	-	100	W
	Either Output	-	-	70	-	-	70	W
Current <sup>2,5</sup>	Either Output	-	-	5.83	-	-	4.66	A
Ripple Voltage	20 Hz to 10 MHz	-	-	100	-	-	100	mVpp
Line Regulation	+V <sub>out</sub> , V <sub>in</sub> = 16 V to 40 V	-	-	20	-	-	20	mV
	-V <sub>out</sub> , V <sub>in</sub> = 16 V to 40 V	-	-	100	-	-	100	mV
Load Regulation	+V <sub>out</sub> , No Load to Full Load	-	-	100	-	-	100	mV
	-V <sub>out</sub> , No Load to Full Load	-	-	150	-	-	150	mV
Cross Regulation, -V <sub>out</sub>	+V <sub>out</sub> : 70% load, -V <sub>out</sub> : 30% load +V <sub>out</sub> : 30% load, -V <sub>out</sub> : 70% load	-	-	450	-	-	450	mV
Load Fault Power Dissipation	Overload <sup>3</sup>	-	-	24	-	-	24	W
	Short Circuit	-	-	24	-	-	24	W
<b>OUTPUT DYNAMIC</b>								
Load Step, Half to Full Load	Output Transient	-	-	600	-	-	600	mVpk
	Recovery <sup>1</sup>	-	-	300	-	-	300	µs
Line Step <sup>3</sup> , V <sub>in</sub> = 16 V to 40 V	Output Transient	-	-	900	-	-	1200	mVpk
	Recovery <sup>1</sup>	-	-	300	-	-	300	µs
Turn On, V <sub>in</sub> = 0 to 28 V	Delay	-	6	10	-	6	10	ms
	Overshoot	-	0	50	-	0	50	mVpk
<b>FUNCTION</b>								
INH Pin Input <sup>3</sup>	Output Inhibited	0	-	1.5	0	-	1.5	V
INH Pin Open Circuit Voltage <sup>3</sup>	Output Enabled	-	3.1	14	-	3.1	14	V
SYNC Frequency Range	V <sub>H</sub> - V <sub>L</sub> = 5V, D = 50%	300	-	380	300	-	380	kHz
Voltage Trim Range		-20	-	10	-20	-	10	%
<b>GENERAL</b>								
Efficiency		86	88	-	87	89	-	%
Capacitive Load <sup>3</sup>	Either Output	-	-	500	-	-	500	µF
Switching Frequency		300	-	380	300	-	380	kHz
Isolation	500 V DC, T <sub>case</sub> = 25 °C	100	-	-	100	-	-	MΩ
Weight		-	-	76	-	-	76	g
MTBF (MIL-HDBK-217F)	GM @ T <sub>case</sub> = 55 °C	-	393	-	-	393	-	kHr

1. Time for output voltage to settle within 1% of its nominal value.
2. Derate linearly to 0 at 115 °C.
3. Verified by initial electrical design verification. Post design verification, parameter shall be guaranteed to the limits specified.
4. Half load at +V<sub>OUT</sub> and half load at -V<sub>OUT</sub>.
5. Up to 70% of the total power or current can be drawn from either of the two outputs.

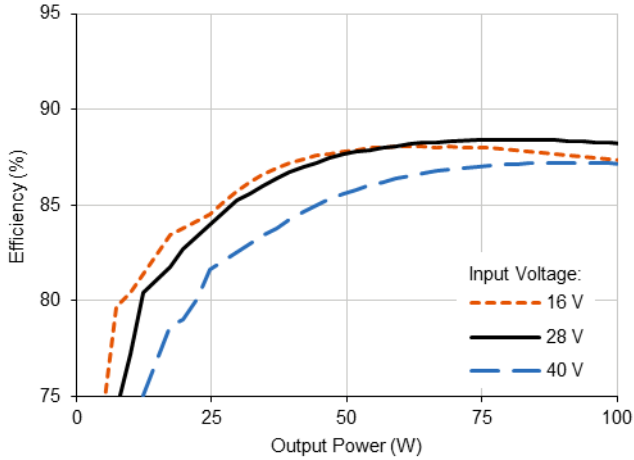


**4.0 PERFORMANCE CURVES**

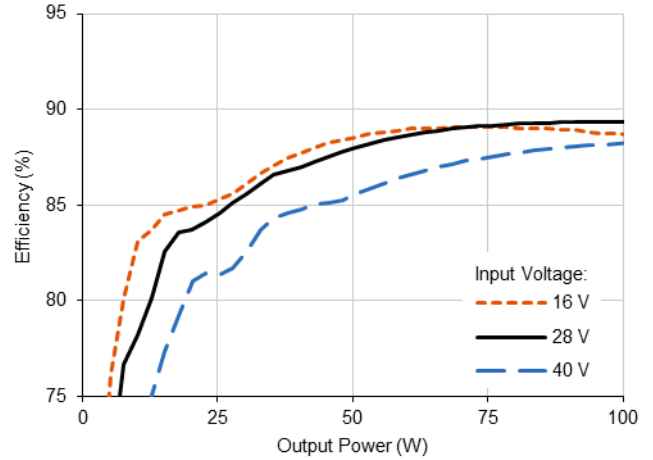
T<sub>case</sub> = 25 °C, Full Load, Unless Otherwise Specified

**4.1 EFFICIENCY PERFORMANCE CURVES**

**4.1.1 VPT100+2812D Efficiency (%) vs. Output Power (W)**

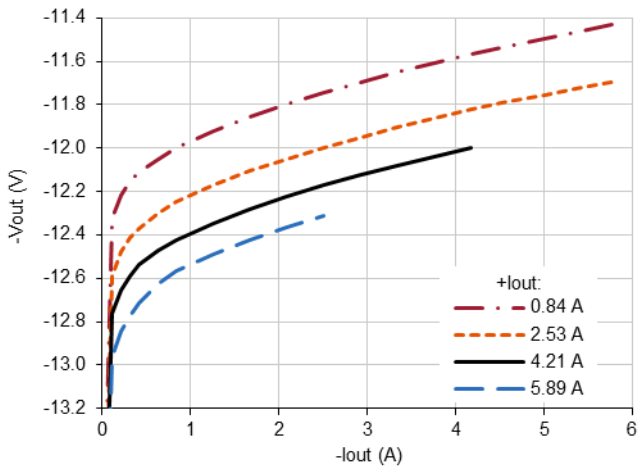


**4.1.2 VPT100+2815D Efficiency (%) vs. Output Power (W)**

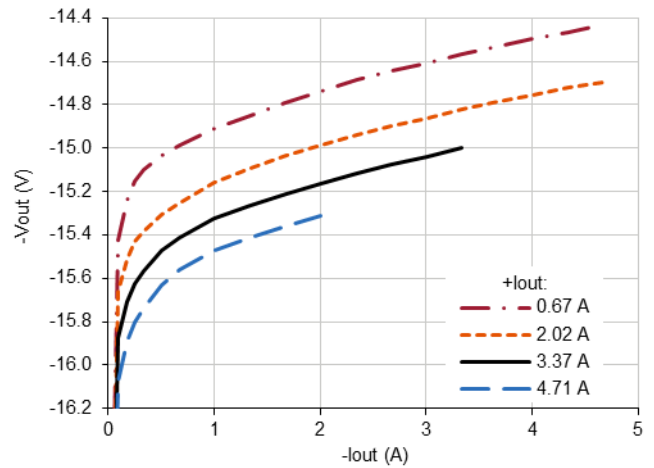


**4.2 CROSS REGULATION CURVES**

**4.2.1 VPT100+2812D -Vout (V) vs. -Iout (A)**

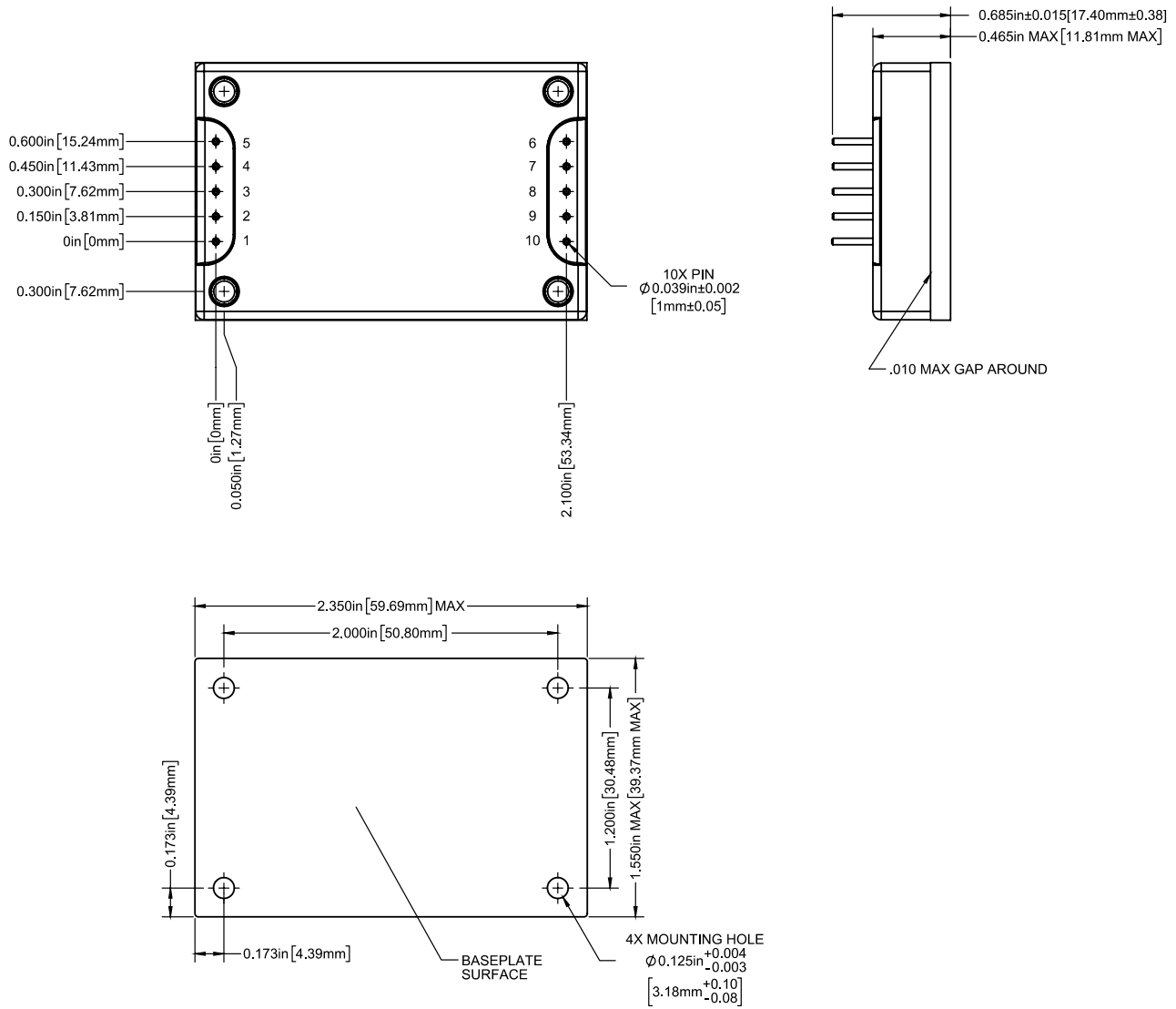


**4.2.2 VPT100+2815D -Vout (V) vs. -Iout (A)**



5.0 MECHANICAL OUTLINES AND PINOUT

Standard Package:



1. Case temperature is measured on the center of the baseplate surface.
2. Materials: Baseplate (Aluminum, conductive conversion coating); Cover (Brass, Nickel Plated); Pins (Copper, gold over nickel plating)
3. Mounting holes are not threaded. Recommended fastener is 4-40
4. This Package is not hermetic.
5. Tolerances are  $\pm 0.005"$  unless otherwise stated.

Pin	Function	Pin	Function
1	28VIN	6	-VOUT
2	SYNC	7	NC
3	INHIBIT	8	OUTCOM
4	CASE	9	TRIM
5	INCOM	10	+VOUT

## 7.0 PACKAGE PIN DESCRIPTION

Pin	Function	Description
1	28VIN	Positive Input Voltage Connection
2	SYNC	Input Synchronization Signal. TTL squarewave, 5 Vpp, 20 - 80% duty cycle.
3	INHIBIT	This is an open collector input. Logic Low = Disabled Output. Connect the inhibit pin to input common to disable the output. Unconnected, open collector or open drain = Enabled Output.
4	CASE	Case Connection
5	INCOM	Input Return Connection
6	-VOUT	Negative Output Voltage Connection
7	NC	No Connection.
8	OUTCOM	Output Return Connection
9	TRIM	Trim Output Voltage to +10%, -20% of Nominal Value. Leave open if not used.
10	+VOUT	Positive Output Voltage Connection

## 7.0 OUTPUT VOLTAGE TRIM

The output voltage can be trimmed down by connecting a resistor between the TRIM pin and the +VOUT pin, or can be trimmed up by connecting a resistor between the TRIM pin and the OUTCOM pin as shown in Section 2.2. The maximum trim range is +10% up and -20% down. The appropriate resistor values versus the output voltage are given in the trim table below.

VPT100+2812D		VPT100+2815D	
+Vout (V)	Rtrim ( $\Omega$ )	+Vout (V)	Rtrim ( $\Omega$ )
13.2	1.14k	16.50	686
13.0	2.39k	16.25	1.69k
12.8	4.26k	16.00	3.19k
12.6	7.39k	15.75	5.7k
12.4	13.6k	15.50	10.7k
12.2	32.4k	15.25	25.9k
12.0	--	15.00	--
11.8	548k	14.75	551k
11.6	266k	14.50	270k
11.4	172k	14.25	175k
11.2	126k	14.00	128k
11.0	97.4k	13.75	99.5k
10.8	78.6k	13.50	80.6k
10.6	65.3k	13.25	67k
10.4	55.2k	13.00	56.9k
10.2	47.4k	12.75	49k
10.0	41.1k	12.50	42.6k
9.8	36k	12.25	37.5k
9.6	31.8k	12.00	33.2k



### 8.0 ENVIRONMENTAL SCREENING

Screening	Condition
Internal Visual	IPC-A-610, Class 3
Temperature Cycling	MIL-STD-883, Method 1010, Condition B, -55°C to +125°C, 10 Cycles
Burn-In	96 hours at +105°C
Final Electrical	100% at 25 °C
External Visual	Internal Procedure

### 9.0 ORDERING INFORMATION

VPT100+	28	05	D
1	2	3	4

(1) Product Series	(2) Nominal Input Voltage	(3) Output Voltage	(4) Number of Outputs
VPT100+	28 28 Volts	12 15 12 Volts 15 Volts	D Dual

Please contact your sales representative or the VPT Inc. Sales Department for more information concerning additional environmental screening and testing, different input voltage, output voltage, power requirements, and source inspection.

### 10.0 CONTACT INFORMATION

To request a quotation or place orders please contact your sales representative or the VPT, Inc. Sales Department at:

**Phone:** (425) 353-3010  
**Fax:** (425) 353-4030  
**E-mail:** vptsales@vptpower.com

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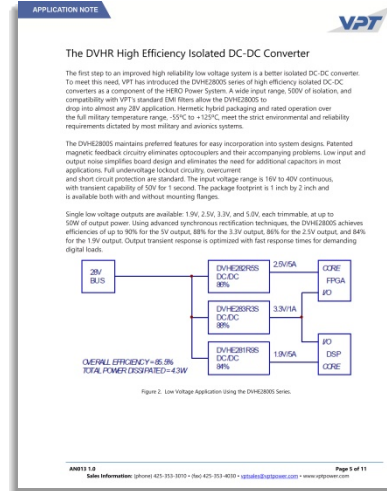
## 11.0 ADDITIONAL INFORMATION

Visit the VPT website for additional technical resources, including:

### [Product Literature](#)



### [Application Notes and White Papers](#)



### [Technical Video Labs](#)



### [Additional Products For Avionics/Military, Hi-Temp, Hi-Rel COTS, and Space Applications](#)

