

Q-PAC FAN WIRING GUIDE

BASIC CONTROLS V3.2.0



EXTERNAL CONNECTIONS

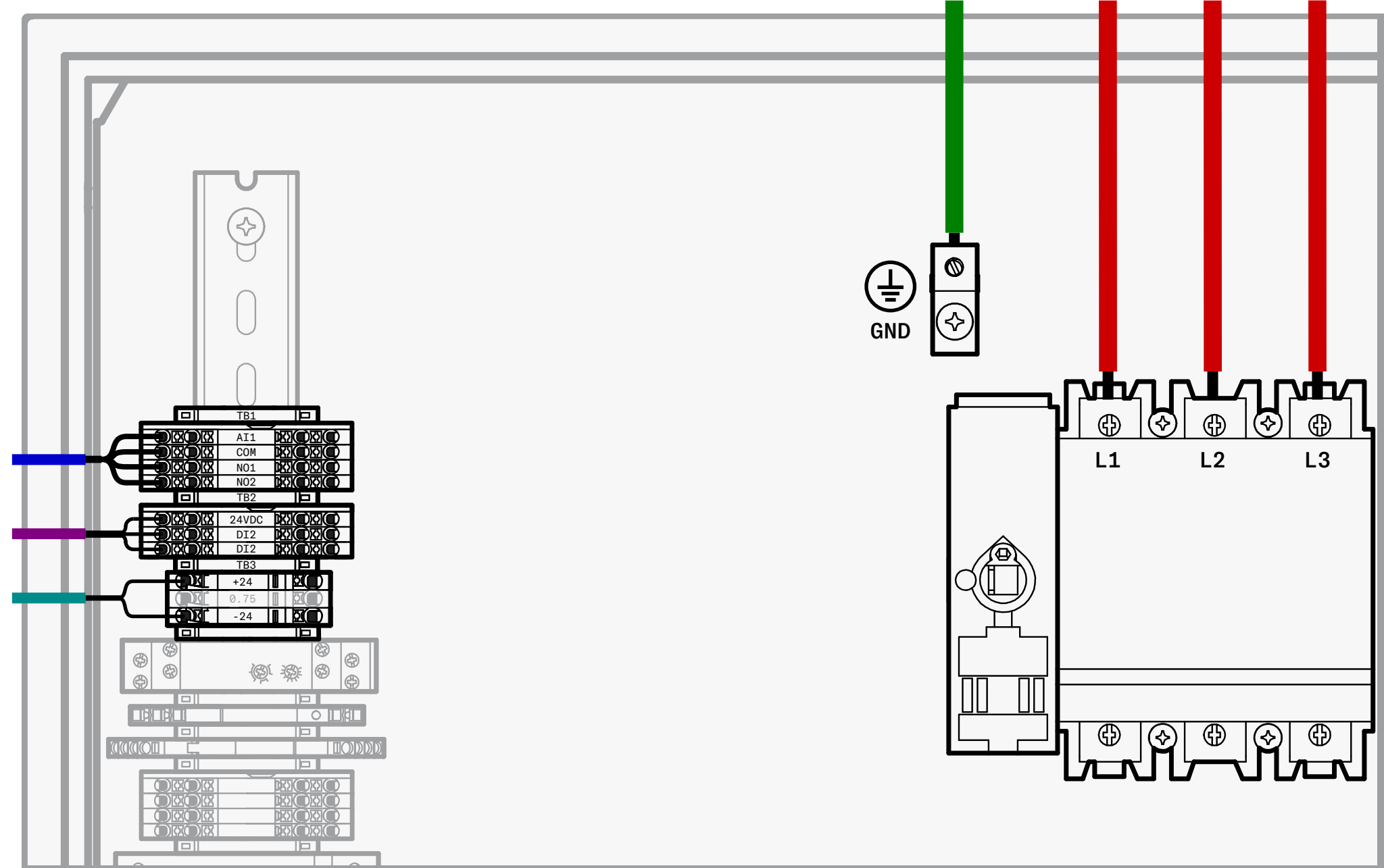
Use this wiring guide to connect the Q-PAC Fan with a Q-PAC Basic Control Panel to your BMS or other HVAC control system. For details on panel functions and settings, check the Q-PAC User Manual for Basic Controls.

TB 1			REQ
AI1	Speed control, 0-10VDC	Analog Input	X
COM	Reference for AI1	Analog Input	X
NO1	Normally open circuit: System is in fault		
NO2	Closed circuit: System is OK		
TB 2			
24VDC	Output for Digital Input controls (DI1, DI2), 24VDC	Digital Output	X
DI1	Safety Circuit	Digital Input	X
DI2	Start/Stop	Digital Input	X
TB 6			
+24	Supplemental power supply for field-supplied sensors and controls, 24VDC output, 0.75A maximum		
-24			
FUSED DISCONNECT SWITCH			X
L1/L2/L3 3 Phase 208V or 480V input voltage, per Fan Controller Nameplate			
EARTH GROUND			X

NOTES:

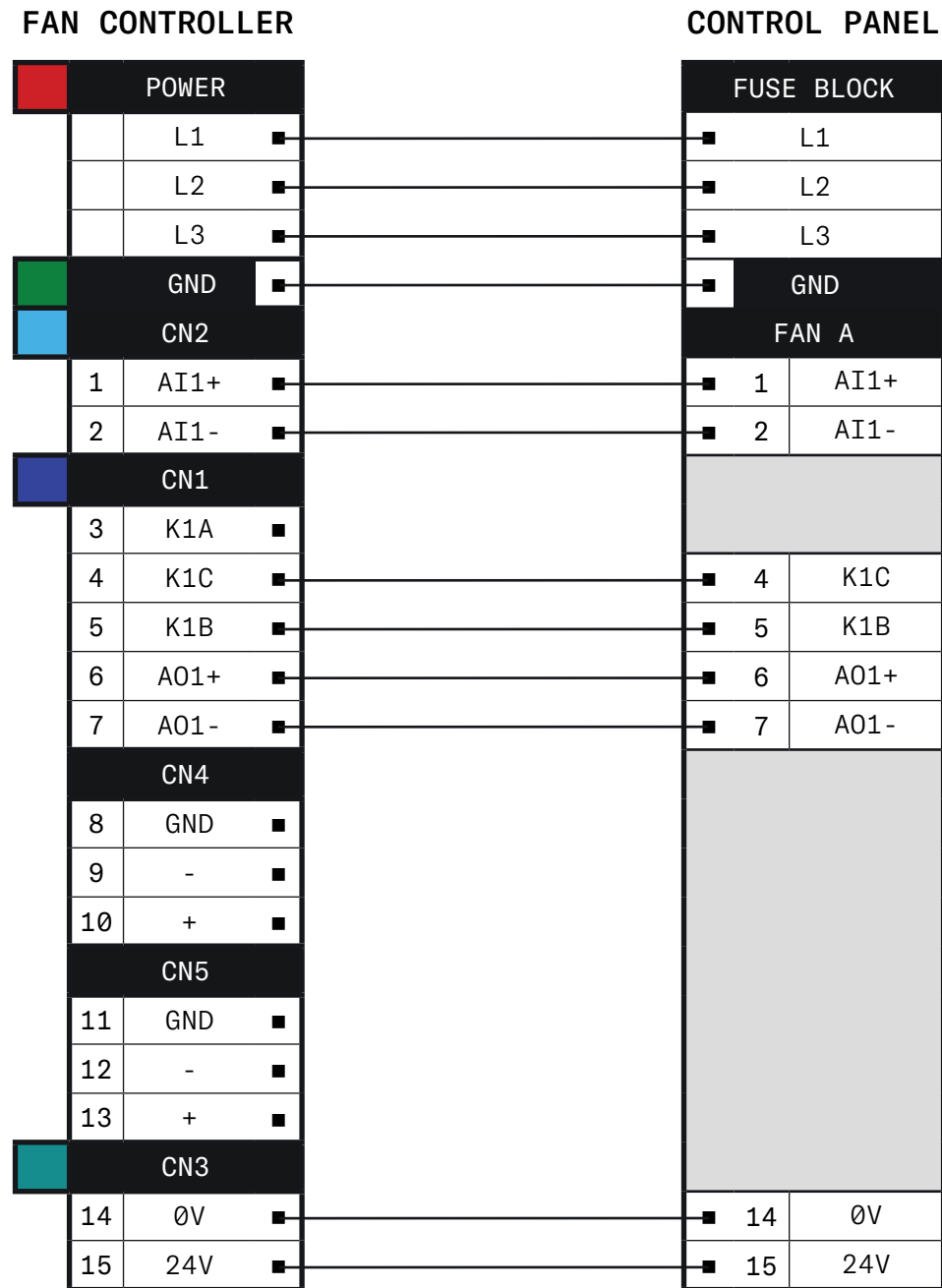
1. Refer to the document pouch included with the Control Panel for detailed panel schematics and support information.
2. Conductor size to be determined by NEC and Control Panel FLC, as listed on the Control Panel nameplate.
3. All colored lines indicate wiring to be completed by others in the field. All open-ended lines indicate connections to an external device or system.
4. Component drawing sizes and positions adjusted for clarity. Actual components may vary with vendor availability and lead time.

BASIC CONTROL PANEL



FAN CONTROLLER CONNECTIONS

This page outlines the Fan Controller Board connections for the fan system. This fan will not connect to CN4 or CN5 (Terminals 8-13). For details on Fan Controller terminals or Modbus functions, check the Q-PAC User Manual for Basic Controls.



- NOTES:**
1. Conductor size to be determined by NEC and Q-PAC Fan MCA as listed on the Fan Controller nameplate.
 2. Min 24 AWG (stranded CU) to CN1, CN2, and CN3 terminals.
 3. All colored lines indicate wiring to be completed by others in the field.
 4. Component drawing sizes and positions adjusted for clarity. Actual components may vary with vendor availability and lead time.

